

DATENBLATT / DATASHEET



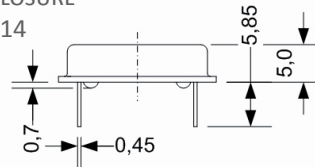
Specification: QTVCXO-DIL8

Oszillator: VCXO

Date: 27.03.2009
ROHS compliance



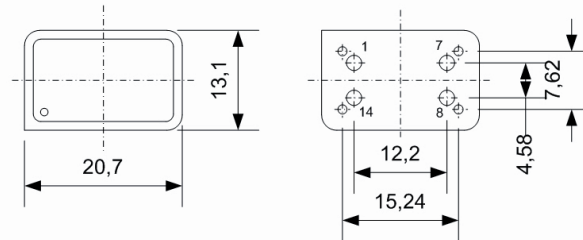
ENCLOSURE
DIL 14



- 1 frei
- 7 Masse
- 8 HF-Ausgang
- 14 Betriebsspannung

PIN CONNECTIONS

- 1 V_c
- 7 Gnd
- 8 RF output
- 14 V_{DD}



PARAMENTER	MIN	TYP	MAX.	UNIT	NOTE
Frequency range	10		40	MHz	¹⁾
Frequency stability					
Nominal tolerance		± 3		ppm	$V_c = V_{dd} / 2$
vs. temperature in operating temp. range		± 5.0		ppm	
vs. supply voltage			± 0.2	ppm	
vs. load change			± 0.2	ppm	
aging			± 2	ppm	+25°C
Frequency adjustment					
Voltage control sensitivity	7		15	ppm / V	
Voltage control range	0		V_{DD}	V	
slope ($\Delta f / \Delta V_c$)		positive			
Supply voltage V_{DD}		3.3		V	
Current consumption		6		mA	
RF output					
Signal waveform		CMOS			
load		10		pF	
duty cycle		45 - 55		%	
rise / fall time		3		ns	
start-up time		2		ms	
Operating temperature range	-40		+85	°C	
Storage temperature range	-45		+120	°C	
Phase noise		<-120		dBc/Hz	at 100Hz
Enclosure	DIL-14				(see drawing)
ESD rating (HBM)			2	kV	

Note: ¹⁾ $f_{min} = 5.0$ MHz, divider $f_0 / 2$